

EXPRESS MAIL NO. EV301025214US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the Applicants' duty to disclose information which may be material to the examination of this application, the undersigned respectfully requests that the Examiner consider on the merits the documents listed on the enclosed Form PTO-1449 (modified) before issuing the first Office Action on the merits.

A copy of the document listed on the enclosed form PTO-1449 (modified) is enclosed herewith.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made (37 C.F.R. 1.97(g)), an admission that the information cited is, or is considered to be, material to patentability, or that no other material information exists.

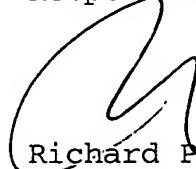
The filing of this Information Disclosure Statement shall not be construed as an admission against interest in any manner. (Notice of January 9, 1992, 1135 O.G. 13-25, at 25.)

Information Disclosure Statement
December 12, 2003
Page 2

The person making this statement is the practitioner who signs below on the basis of information supplied by an individual associated with the filing and prosecution of this application (37 C.F.R. § 1.56(c)) and on the basis of information in the practitioner's file.

If any fees are due, please charge our Deposit Account No. 12-0415 or credit any overpayment to our Account No. 12-0415. No fees should be due because this Information Disclosure Statement is being filed concurrently with the above-identified U.S. patent application.

Respectfully submitted,



Richard P. Berg
Attorney for Applicant
Reg. No. 28,145

LADAS & PARRY
5670 Wilshire Boulevard
Suite 2100
Los Angeles, CA 90036
(323) 934-2300

Enclosures: Form PTO-1449 (modified) (1 page)
Copies of documents listed in Form PTO-1449 (modified)

Information Disclosure Statement

December 12, 2003

Page 3

Form PTO-1449 (Modified)	ATTY DOCKET NO. B-5320 621551-4	U.S. SERIAL NO. not yet assigned
LIST OF PATENTS AND PUBLICATIONS STATEMENT	APPLICANT(S) Cheng-Wen WU, et al.	
	FILING DATE concurrently herewith	GROUP not yet assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE or 102(e) DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Chi-Feng Wu, et al., "Simulation-Based Test Algorithm Generation and Port Scheduling for Multi-Port Memories," Proceedings of the 38th Design Automation Conference, DAC 2001, June 18-22, 2001, Las Vegas, NV, USA	
--	---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------	--

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.